Communication protocol description

(MODBUS-RTU-V2.2)

一、 **MODBUS** communication

Network intelligent panel can read and modify internal registers through 485 communication to achieve remote monitoring and output control. Communication protocol adopts standard MODBUS RTU communication protocol. Serial port settings: baud rate "9600", check bit "none", data bit "8" and stop bit "1".

Two instructions are separated by 50ms.

1.1 communication format

Communicati	Command	data address	Communicati	CRC check
on number	code		on data	
1Byte	1Byte	2Byte	nByte	2Byte

1.2 communication number

1--255 switch factory default address: 0x02; 0XFF is the broadcast address.

No data is returned from the slave in broadcast mode.

1. 3 command code

Command	Operation content
code	
03H	Read the specified address data
06H	Setting address data
10H	Setting multiple address data
83H	Error return of reading instruction
86H	Error in setting the address.
90H	Error in setting multiple addresses.

Error code

Lifti couc		
Error code	meaning	explain
01H	Check error	
02H	operation mistake	
03H	Read only register	
04H	Register does not	
	exist.	
05H	Invalid operation for.	
06H	The register is write	
	protected.	

1.4 CRC16-1 check algorithm

At the beginning of CRC, all 16 bits of the register are set to "1", and then two adjacent 8-bit bytes of data are put into the current register. Only the 8-bit data of each character is used to generate CRC, and the start bit, stop bit and parity bit are not added

to CRC. During CRC generation, every 8-bit data is XOR-operated with the value in the register, and the result is shifted one bit to the right (in the direction of LSB), and the MSB is filled with "0" to detect the LSB. If the LSB is "1", it will be XOR-operated with the preset fixed value, and if the LSB is "0", it will not be XOR-operated. Repeat the above process until it is shifted 8 times. After the 8th shift, the next 8-bit data is XOR with the current value of this register. After all information processing, the final value in the register is CRC value.

The process of generating CRC:

- 1. set the 16-bit CRC register to FFFFH.
- 2. The first 8-bit data is XOR with the lower 8 bits of CRC register, and the result is put into CRC register.
- 3. The CRC register is shifted one bit to the right, the MSB is filled with zero, and the LSB is checked.
- 4. (If LSB is 0): Repeat 3, and then move one bit to the right. (if LSB is 1):CRC register is XOR with 0xA001.
- 5. Repeat 3 and 4 until 8 shifts are completed, and the processing of 8-bit bytes is completed.
 - 6. Repeat steps 2 to 5 to process the next 8-bit data until all bytes are processed.
 - 7. The final value of CRC register is CRC value.
- 8. When the CRC value is put into the information, the lower 8 bits come first and the upper 8 bits come last.
- 1.4 communication example (switch communication example, the following communication data are hexadecimal)

(1) read a single address data, for example, the upper computer sends the following command:

Communicati	Command	data address	Communicati	CRC check
on number	code		on data	
02	03	10 0B	00 01	F1 3B

Example: 02 03 10 0B 00 01 F1 3B

Command explanation: Read key values.

If the first key is pressed, the returned data is.

Communicati on number	Command code	Data length	Communicati on data	CRC check
02	03	00 02	07 01	27 C9

(2) Send data to light the status light of the first button:

Communicati	Command	data address	Communicati	CRC check
on number	code		on data	
02	06	10 08	01 01	CC AB

Explanation: turn on the first key status light and backlight.

The lower computer returns the following data to prove that the modification is successful.

Communicati	Command	data address	Communicati	CRC check
on number	code		on data	
00	0.6	10.00	01.01	CCAD

III. Register address mapping table of intelligent switch panel

(The register is double-byte data, $0x1000 \sim 0x1FFF$, with the highest bit in front).

Read-write integrated instruction 07

	Panel	Comm	function	Relay value	Indicator	CRC
	address	and	code		value	check
		code				
Upper computer sending	02	07	00-04	relay	Indicator light	91 23
Panel reply	02	07	Key state	Relay status	Indicator	check
					status	code

Note: See register 100B for key values, register 1009 for relay values and register 1008 for indicator values.

- ① The function code of 00 00 means that you don't need to reply, just write the relay and indicator register, that is, only update the status of the panel relay and indicator; [Write only]; (write only, no data returned)
- ② The function code of 00 01 indicates that data needs to be replied, and the data of relay-indicator light-key register value should be answered.

Do not update the status of panel relays and indicator lights, and fill in the values of relays and indicator lights at will; [Read-only] (Read the panel keys and the status of indicator lights)

- ③ The function code of 00 02 means that the response is made only when the key changes, and the status of the panel relay and indicator light is updated, and the value data of relay-indicator light-key register is returned; [After successful writing, return the value of successful modification]
- ④ The function code of 00 03 means that the key changes before the reply, and the relay-indicator light-key register value data is returned; [read only]

(The key state and indicator light state are returned only when the key is pressed)

- ⑤ The function code of 00 04 indicates that data needs to be replied, the status of panel relay and indicator light is updated, and the value data of relay-indicator light-key register is replied; [After successful writing, return the value of successful modification]
 - ⑥ When the address 0XFF is broadcast instruction, operate all panels, reply with key action (function code is 02 or 03), and do not reply without key action;

Communication number of intelligent switch panel: 1-255, factory default address: 0x02; 0XFF is the broadcast address.

Register	Register description
address	
(hexadecima	
1)	
1000 (read	[Note: This register is used for system configuration, and the factory default
and write)	value is 02. If you don't need to modify it, you can ignore it]
,	1. Panel address register, the default register data is 0x02; ;
	Example of instruction:
	2 06 10 00 00 01 4cf9 is set to address 01.
	06 10 00 00 01 59 14 is set to address 1 (broadcast mode is valid for all panels)
	2. Complete the change to send the save instruction: FF 06 10 0F 00 FE 29 57
	Address change under 485 connecting multiple panels is supported. Operation

status light flashes. Send the data at the beginning of FE to change the address (for example, the panel that changes address 5 sends FE60 10 00 05 59 06). After the panel change is successful, the white status light stops flashing. According to the customer's proposal, the big keys of V8-Z6 panel are separated into two small keys. B6-B15 B5 B4 B3 B2 B1 B0 reserve The third The The The The The Second Reys the serve of high of keys keys is keys is light. Configuration key: the high bit is 0 and the low bit is 1. Configure a pain of evelopment send: 02 06 10 10 00 15 1D 36 To configure a pain of evelopment send: 02 06 10 10 00 15 1D 36 To configure a pain of evelopment send: 02 06 10 10 00 15 1D 36 To configure a pain of evelopment send: 02 06 10 10 00 15 1D 36 To configure a pain of evelopment send: 02 06 10 10 00 15 1D 36 To configure a pain of evelopment send: 02 06 10 10 00 15 1D 36 To configure a pain of the customer's proposal, the V8-Z6 panel large aperture indicator lights are separated into the left and right sides. B6-B15 B5 B4 B3 B2 B1 B0 The The The The The Reserved group of third second point in the parture position of high aperture position of high aper		method: Pres				-	•			
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03 0B C6 instruction. [Note: This register is used for system configuration. If it doesn't need to be modified, users can ignore it] 1007 001 turn on the radar, 00 00 turn off the radar. 1008 (read and write) Display control of indicator lights D1 ~ D16 B15- B8 B7 B6 B5 B4 B3 B2 B1 B0 B15- B9 B8 B7 B6 B5 B4 B3 B2 B1 B0 When a bit is 1, the corresponding key LED is lit, and when a bit is 0, the LED goes out.							2 0	• 1 00 10		
[Note: This register is used for system configuration. If it doesn't need to be modified, users can ignore it] 1007 001 turn on the radar, 00 00 turn off the radar. 1008 (read and write) Display control of indicator lights D1 ~ D16 B15- B8 B7 B6 B5 B4 B3 B2 B1 B0 B16- B9 B6 B5 B4 B3 B2 B1 B0 Backlig 6 key 5 key 4 key 3 key 2 key 1 key			•		effective c	ommand o	of FF 06	21 99 10		
modified, users can ignore it] 1007 001 turn on the radar, 00 00 turn off the radar. 1008 (read and write) Display control of indicator lights D1 ~ D16 B15- B8 B7 B6 B5 B4 B3 B2 B1 B0 B16- B9 B8 B7 B6 B5 B4 B3 B2 B1 B0 When a bit is 1, the corresponding key LED is lit, and when a bit is 0, the LED goes out.					~					
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1008 (read and write) Display control of indicator lights D1 \sim D16 B15- B8 B7 B6 B5 B4 B3 B2 B1 B0 B0 B0 B5 B4 B3 B2 B1 B0 B0 B0 B5 B4 B3 B2 B1 B0 B0 B5 B4 B3 B2 B1 B0 B0 B5 B4 B5 B4 B5 B4 B5 B1 B0 B0 B5 B4 B5 B4 B5 B1 B0 B0 B5 B4 B5 B1 B0 B1 B0 B5 B4 B5 B1 B0 B1 B0 B1 B0 B1 B1 B0 B1 B1 B0 B1 B1 B1 B0 B1		modified, use	rs can ignor	e nj						
1008 (read and write) Display control of indicator lights D1 \sim D16 B15-B9 B8 B7 B6 B5 B4 B3 B2 B1 B0 When a bit is 1, the corresponding key LED is lit, and when a bit is 0, the LED goes out.	1007	001 turn on	the radar	00 00 to	rn off the m	adar				
and write) B15- B9 B8 B7 B6 B5 B4 B3 B2 B1 B0 Backlig ht LED										
B9 Backlig 6 key 5 key 4 key 3 key 2 key 1 key led When a bit is 1, the corresponding key LED is lit, and when a bit is 0, the LED goes out.) R1	B0		
When a bit is 1, the corresponding key LED is lit, and when a bit is 0, the LED goes out.	and write)	B9	B9							
When a bit is 1, the corresponding key LED is lit, and when a bit is 0, the LED goes out.								-		
0, the LED goes out.					l e					
				F 32	<i>3 , -</i>		, <u></u> -			
1			•	n:						
· · · · · · · · · · · · · · · · · · ·										

2 06 10 08 01 01 CCAB key 1 LED is on and its back is on (other key LEDs are off) 02 06 10 08 01 00 CC AB back is bright, and all key LEDs are off. 2 06 10 08 00 000cfb All key LEDs are off (including backlight)

1009 (read and write, ignore the weak current panel) relay control

B15- B9	В8	В7	В6	В5	B4	В3	B2	B1	В0
	-	-	-	-		4 relay	3 relay	2 relay	1 relay

When a bit is 1, the corresponding relay is closed, and when a bit is 0, the relay is open.

Example of instruction:

Open the first relay: 02 06 10 09 00 01 9C FB Open the second relay: 02 06 10 09 00 02 DC FA

Open the first and second relays: 02 06 10 09 00 03 1D 3A

Turn off all relays: 02 06 10 09 00 00 5D3

100B (read only)

(read | Key value register

Bit $0 \sim$ bit 5 correspond to key 1 to key 6, respectively. Bit8 \sim Bit15 are absolute key values.

B15- B8	B7-B6	B5	B4	В3	B2	B1	B0
Absolute key		=0 key 6 pops up,	=0 key 5 pops up,	=0 key 4 pops up,	=0 key 3 pops up,	=0 key 2 pops up,	=0 key 1 pops up,
value		=1 Press key 6.	=1 Press key 5.	=1 Press key 4.	=1 Press key 3.	=1 Press key 2.	=1 Press key 1.

Absolute key value = (panel address-1) * 6+key number (Key number: Key 1 is numbered 1, key 2 is numbered 2 ... and so on.)

Example of instruction:

Read the key value, key 1 is pressed, and the panel address is 02.

Upper computer sending: 0203100B 001F13B

Panel back: 02 03 00 02 07 01 27 C9 (key value is 7)

Old version: no key value.

B15- B8	B7-B6	B5	B4	В3	B2	B1	В0
		=0 key 6 pops up,	=0 key 5 pops up,	=0 key 4 pops up,	=0 key 3 pops up,	=0 key 2 pops up,	=0 key 1 pops up,
		=1 Press key 6.	=1 Press key 5.	=1 Press key 4.	=1 Press key 3.	=1 Press key 2.	=1 Press key 1.

You can use FD data to start querying the panel. The panel will not reply to the data without pressing a key, but only the panel with pressing a key will reply to the data. For example, the instruction is FD 03 10 0B 00 01 E5 34.

100F (write only)

[Note: This register is used for system configuration. If the user does not modify the system configuration (such as modifying the panel address), it will be ignored]

Save control register:

Writing 00FE panel to this register will save the configuration, and every time

	the syste save ins Example 02 06 10	truction e of inst	will be s ruction:	sent, and						dress), a
310 ~ 131f	Key K1 ~ K16 status bits (0: off, 1: on, 2: long press the flag)									
(read only)	Example of instruction:									
	The upper computer sends the reading instruction: 02 03 13 10 00 01									
	81 78 Read button 1 status.									
	1 Press the key to pop up the panel, and it will return to: 02 03 13 10 00 00 40									
	B8.									
	1 Press the key to return to the panel: 02 03 13 10 00 01 81 78.									
1045 (read	1 Press the panel for a long time to return: 02 03 13 10 00 02 C1 79.						ed to be			
and write)	[Note: This register is used for system configuration. If it doesn't need to be modified, users can ignore it]									
and write)	Modify the baud rate register, the factory default baud rate is 9600, and the									
			0X48 (t	oaud rate	e 4800) o	or 0X96	(baud rate 9600) or 0XC0 (baud			
rate 19200).										
1046 (read	To send the save instruction: FF 06 21 99 10 45 8A 34 [Note: This register is used for system configuration. If it doesn't need to be									
and write)	modified							11 10 000		
	D 1.1	. 1	. ,	1	1	11	1 .		.1	1 1
										nel reply ns (save
	instructi			18 131	iis, aiid	the set	ung rai	1gc 15 2	20~200II	15 (Save
				the data	a reply	after 200	Oms: FF	06 10 4	46 00 C	8 78 97,
	send the									
10D0 (read		ngle-byt	te and d	ouble-by	yte conf	iguration	ı (factor	y defau	lt is dou	ble-byte
and write)	panel) Change	double-l	byte date	a: 02 06	10 D0 0	0 00 80	C0			
	Change single byte data: 02 06 10 D0 00 01 4D 00 The change of sending instruction takes effect: 02 06 21 99 10 D0 5F B6									
10D2 (read	Increas	se the s	status (of indic	cator li	ght: th	e high	byte o	of key_1	register
and write)						_		_		option,
		•					e of ke	ys, and	d the st	atus of
	indicator lights after configuration.									
	B15- B9	B8	B7	B6	B5	B4	B3	B2	B1	B0
									1 key register	
									high	
									byte indicat	
									or	
	The high byte of the configuration key register indicates the indicator light status									
	data instruction: 02 06 10 D2 00 02 AC C1									
	The high byte of the configuration key register indicates the absolute key value									
	data instruction: 02 06 10 D2 00 00 2D 00 Save instruction takes effect: 02 06 21 99 10 D2 DE 77									
	Save instruction takes effect: 02 00 21 99 10 DZ DE //									

1 99 only)	(write	Configuration register save instruction When modifying the corresponding register data, it should be written into the configuration register to save, and the modified information will take effect; 06 21 99 * * * check code (* * * indicates the modified register)

IV. Address Mapping Table of Power Access Register of High Frequency Card (The register is double-byte data, $0x0200 \sim 0x02FF$, with the highest bit in front).

The number of the high-frequency card-inserting and power-taking communicator is 1-255, and the factory default address is 0x01; 0X00 is the broadcast address.

Register address (hexadecimal)	Register description	Allow reading and writing
0201	Device status register, =0000 without card, =0001 with card inserted Example of instruction: The upper computer sends and reads instructions: 01 03 02 01 00 01 D4 72. If there is no card, return: 01 03 00 02 00 00 E4 0A If there is a card, it will be returned: 01 03 00 02 00 01 25 CA	
0202	Card type 1-255, 0 is no card. 00= leaving status, 01= general control card, 02= building number card, 03= floor card, 04= cleaning card, 05= engineering card, 06= guest card, 07= privilege card. Example of instruction: The upper computer sends and reads instructions: 01 03 02 02 00 01 24 72 If there is no card, return: 01 03 00 02 00 00 E4 0A The total control card returns: 01 03 00 02 00 01 25 CA. The guest will return: 01 03 00 02 00 06 64 08.	read only

Figure 1: Definition of U-shaped aperture panel keys and indicator light aperture numbers

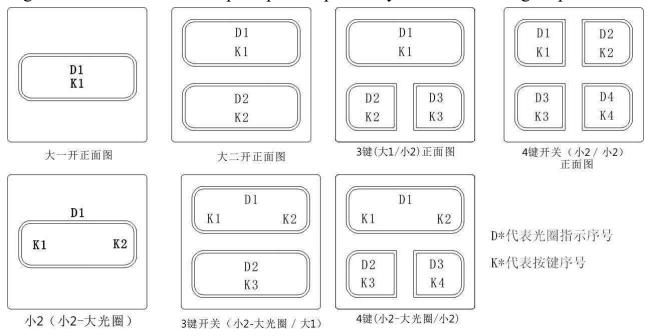


Figure 2: Definition of aperture numbers of keys and indicator lights of oval and square aperture panels: V8-Z,Z1,Z6

4键 (大1/大1/小2)

5键开关(大1/小2/小2)

5键(小2-大光圈/大1/小2)

代

表按键序号